This booklet contains information for advanced programmers who want to modify the keypad handler program or create their own program to read data from the keypad. The ATARI® CX85™ Numerical Keypad Owner's Guide contains additional information with which you should be familiar.

### **Modifying the Keypad Handler Program**

The diskette that came with this package contains a keypad handler program which is written with the ATARI Macro Assembler AMAC. You can modify or rewrite this program with the Program-Text Editor. When your modifications are complete, reassemble the code with AMAC using a unique filename.

#### Hardware Notes: Keypad Interface and Timing

The keypad uses eight signals on the controller port. Positive 5 volts (+5 v) is on pin 7, and signal ground on pin 8. A 5-bit binary code is presented on pins 1 through 5, corresponding to the signals FWD, BACK, LEFT, RIGHT and BPOT. A data valid signal, presented on pin 6, corresponds to TRIGGER. TRIGGER goes low to indicate a valid code.

#### Timing is as follows:

- With no key pressed, the code for the previously pressed key remains on pins 1 through 5 and TRIGGER remains high (logic 1 or True).
- When a key is pressed, the TRIGGER signal goes low (logic 0 or False) and the keycode for that key is established on pins 1 through 5.
- TRIGGER stays low as long as the key remains pressed.
   When the key is released, TRIGGER returns high but the keycode does not change.
- Two-key rollover handles simultaneous or multiple keystrokes. If one or more additional keys are pressed while the first key is still pressed, nothing happens; the additional keys are locked out. When the first key is released, TRIGGER goes high and the scanning electronics searches for the next active key in the sequence. TRIGGER then goes low and the new keycode is presented.

After the system receives the TRIGGER signal there is a slight delay before BPOT data is valid. This can be compensated for by inserting a delay in your program. Details about this timing difference are provided on page 5.

#### **Software Notes**

Signals generated by the keypad electronics are delivered to specific registers in your computer's memory through the joystick port. The keypad makes use of registers normally used for both joystick and paddle controllers. The sequence in which your program reads these signals and the operations your program performs on the signals are both important factors. TRIGGER must be read first to see if a key has been pressed. If a key has been pressed, it must be decoded. Four bits of the 5-bit keycode are sent to the joystick registers and the fifth bit is read through the paddle (BPOT) register. These bits must be manipulated and logically combined for your program to know which key has been pressed. Once the keypad key has been decoded, it can be associated with any key on the keyboard, or any operation for which your keypad handler is designed.

#### **Controller Port Selection**

The handler program, as written, allows the keypad to work only through joystick port 2. However, it can be rewritten to allow the keypad to work through any one of the four controller ports. You'll have to modify the handler to use alternate registers or write those registers into a new program to recognize the port.

#### **Determining a Keypress**

To determine if a key has been pressed, your program must read the OS shadow for the joystick controller port to which the keypad is connected.

For port 1 read STRIGO (\$284).

For port 2 read STRIG1 (\$285).

For port 3 read STRIG2 (\$286).

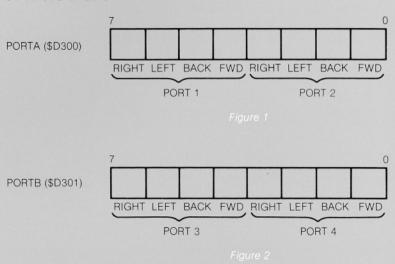
For port 4 read STRIG3 (\$287).

Only the least significant bit is used (bit 0). If a key has been pressed, the LSB will contain a zero (0). If the bit contains a one (1) no key has been pressed. The remaining seven bits will contain zeroes.

#### **Decoding Keys**

Once the STRIG value has been read and a data-valid signal exists, the incoming 5-bit code must be decoded. To do this, you must read the hardware registers, not the OS shadows. Read the joystick byte first.

Registers PORTA (\$D300) and PORTB (\$D301) pick up the first four incoming bits (see Figures 1 and 2). Depending on which port is selected, the bits will come into positions 1 through 4 (bits 0-3), or positions 5 through 8 (bits 4-7). They must be shifted to bits 0 through 3, and bits 4 through 7 must be forced to zero.



**Note:** All shifting and masking operations must be done in the accumulator—do not use memory address shift instructions.

BPOT is extracted from ALLPOT (\$D208)—see Figure 3. All bits in the byte, except the bit for the desired port, must be forced to zero. The chosen BPOT bit must be complemented and shifted so that it's in the fifth position (bit 4).



Finally, a logical OR must be used to integrate the BPOT value and the values of the joystick operation. The resultant binary value, represented in Figure 4, can be decoded from the truth table on pages 6-7.

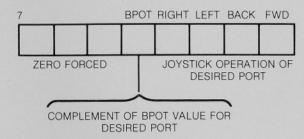


Figure 4

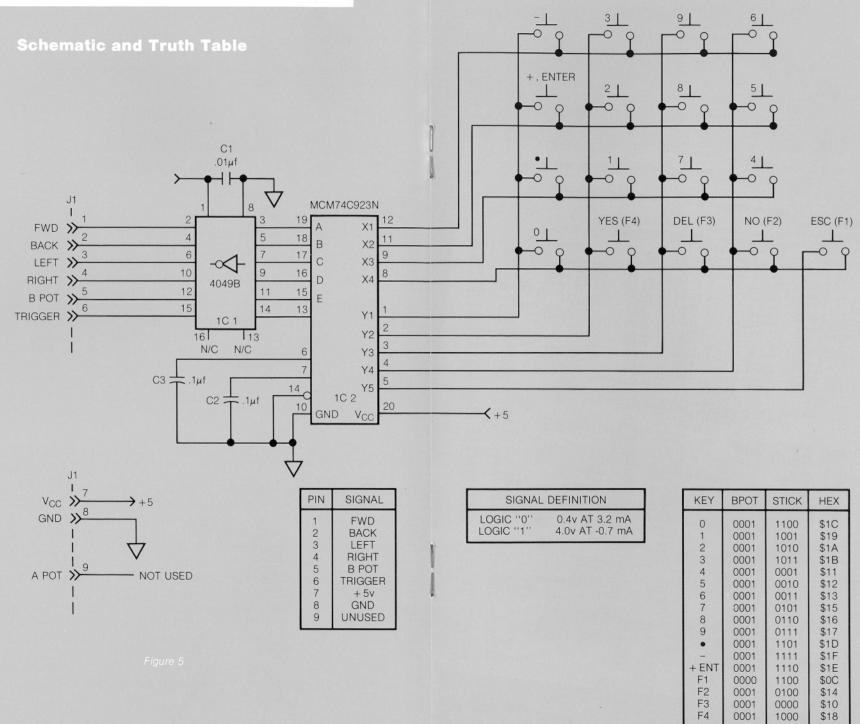
#### **Timing Differences Between TRIGGER and BPOT**

The BPOT value is input to POKEY through a resistor-capacitor delay circuit in your computer. The TRIGGER signal indicating a new keypress occurs instantly, while the BPOT signal might be delayed up to 150 microseconds. Insert an intentional delay in your program to compensate for this time difference—150 microseconds corresponds to approximately 150 NOPs—or insert a loop decrementing a register from 30 to zero.

## **Appending to DOS II**

To use the serial ports on your ATARI 850<sup>TM</sup> Interface Module and load the keypad handler at the same time, you must append KEYPAD.OBJ to the end of the DOS II AUTORUN.SYS file. The interface module is booted automatically with the AUTORUN.SYS file. If you append the keypad handler to the end of this file, the interface module will be booted and your keypad enabled as well. This should be done to a copy of the DOS II Master Diskette using the COPY FILE command in DOS II. Please refer to the ATARI Disk Operating System II Reference Manual for the correct procedures.

If you plan to load a keypad handler and use DOS more than once while programming, your keypad handler diskette must have a MEM.SAV file.



## Keypad Interrupt Handler Source Code

#### ATARI MACRO Assembler Ver 1.0A Page 1 D1:KEYPAD.SRC :DEMONSTRATION OF CX-85 KEYPAD INTERRUPT HANDLER This keypad interrupt handler detects and handles all keys pressed on a CX-35 keypad plugged into port 2. This is assembled using Atari Macro Assembler. START MASK SELECT MASK OPTION MASK BPOT BIT MASK VERTICAL BLANK INTERRUPT VERTICAL BLANK INTERRUPT TRIGGER 1 ATTRACT MODE FLAG ATTRACT MODE FLAG EYBOARD CODE VEYBOARD CODE ALL POT STATUS PORTA ROUTINE FOR SETTING VECTORS WARM START ADDR WARM START ADDR CONSOL SWITCH PORT BREAK KEY FLAG BLED ELSEWHERE EQU \$9 TIMER TIMER1 START = 0030 \$C \$08 \$224 \$285 = 0006 EQU = 0009 SELECT = 000A = 000C = 0008 BPOT \$4D \$2FC \$D208 \$D300 WBLKD EQU = 0224 STRIG1 = 0285 = 004D EQU ATTRACT EQU CH \$E45C = 02FC CUNSUL EQU \$UUT BREAK KEY FLAG BREAK EQU \$11 EQU MAY BE REASSEMBLED ELSEWHERE LOCATED IN PAGE 6 BUT MAY BE REASSEMBLED ELSEWHERE = D208 PORTA SETVBV = D300 = E45C INITIAL ENTRY POINT TO ESTABLISH VBLANK ENTRY SYSTEM RESET KEY RESETS VBLANK VECTORS, SYSTEM RESET KEY DOS INIT HENCE CHAIN TO DOS INIT SAVE VALUE IN DOSINI = 000C = D01F = 0011 WRMEXT + 1 DOSINI+1 WRMEXT+2 = 0600 STA COLDST: 0000 0600 0602 LDA A50C 8D2C06 REPLACE DOSINI WITH WARMST #LOW WARMST A50D #HIGH WARMST DOSINI + 1 STA CHAIN KEYPAD INTO DEFERRED VBLANK PROCESSING SAVE VVBLKD FOR KEYPAD EXIT POINT LDA KPADVBI: 0605 8D2D06 0607 A928 060A 850C 060C 060E A906 850D 0610 EXIT+2 AD2402 0612 0615 0618 REPLACE VVBLKD WITH KEYPAD ENTRY POINT 8DBA06 AD2502 8DBB06 DEFERRED VBI 061B SETVBV LDA JSR RTS A051 A206 A907 061E 0620 ENTERED WHEN USER HITS SYSTEM RESET 0622 205CE4 0624 ENTERED WHEN USER THE REESTABLISH VBLANK VECTOR WARMST: KPADVBI CHAIN TO DOSINI 0627 WARMST: WRMEXT: 201206 4C0000

ATARI MACRO Assembler Ver 1.0A Page 2 D1:KEYPAD.SRC KEYPAD TRANSLATION TABLE 062E 0C0C KPADTAB: FUNCTION 1 0630 0632 1434 DB \$14,\$34 FUNCTION 2 1007 DB \$10,\$07 FUNCTION 3 0634 1826 DB \$18.\$26 FUNCTION 4 0636 DB \$1C.\$32 0638 191F DB \$19,\$1F 063A 1A1F DB \$1A,\$1E 063C 1B1A DB DB \$1B,\$1A 063E 1118 \$11,\$18 0640 DB \$12,\$1D 0642 131B DB DB 1533 \$15,\$33 0646 1635 \$16.\$35 0648 DB \$17,\$30 064A 1D22 \$1D,\$22 0640 1F0E 1E06 DB DB \$1F,\$0E \$1E,\$06 064E + ENTER END OF TABLE 0650 DB ENTERED AT EACH VBLANK TO READ THE KEYPAD 0651 AD8502 KPAD: LDA STRIG1 KEY PRESSED? 0654 D044 A069A KPADDM EXIT FOR KEY NOT PRESSED RESET ATTRACT MODE BNE 0656 LDA ATTRACT 0658 854D DETERMINE VALUE OF KEY PRESSED LDA PORTA 065A AD00D3 ;READ CABLE PIN OF PORT 2 065D LSR 065E 4A LSR 065F 44 LSR 0660 LSR 4A 0661 8DBC06 TEMP STA 0664 AD08D2 LDA ALLPOT READ ALLPOT FOR 5TH CABLE PIN STAT: 0667 2908 AND #BPOT MASK FOR 5TH PIN 0669 4908 EOR #BPOT COMPLEMENT BIT (0 IS VALID) 066B OA ASL 066C 0DBC06 ORA TEMP A HAS KEY VALUE 066F INIT COUNTER SCAN TRANSLATION TABLE KPADCK: :MATCH KEYPAD TABLE ENTRY? 0671 D92E06 CMP KPADTAB,Y F009 ∧067F KPADMAT JUMP IF MATCH 0674 0676 INY INC TO NEXT ENTRY 0677 C8 INY KPADTAB,Y ;END OF TABLE? ;EXIT FOR END OF TABLE BF2F06 0678 LDX 067B F03C A06B9 BEQ EXIT D0F2 0671 **KPADCK** 067D KEY VALUE MATCHES PUT NEW KEYCODE IN CH AND RESET AUTO-REPEAT 067F AA SAVE KEY VALUE 0680 INY GET POKEY KEYCODE ;A HAS KEYCODE ;VECTOR ROUTINE? 0681 B92E06 KPADTAB,Y 0684 COFF CMP #¢FF KPADFUN EXIT FOR VECTOR ROUTINE 0686 F040 A06C8

# Keypad Interrupt Handler Source Code

ATARI MACRO Assembler Ver 1.0A Page 3 D1:KEYPAD.SRC  CMP KPADCOD STA CHARLANDO STA KPADCOD STA CHARLANDO STA CHARL	
Dec	
0603 8DD806 CALL JMP BRKPRS 06D6 200000 KPADFV: JMP BRKPRS 06D9 ADBF06 KPADF1: BEQ 06D0 06D0 A0B00 LDA BREAK 06D0 A0B00 A0B00 BREAK 06D0 A0B00 A0B00 BREAK 06D1 A0B00 A0B00 BREAK 06E1 A0B00 A0B00 BREAK 06E3 8511	

06E5 8DBF06 06E8 A9C0 06EA 8DBD06 06ED 60 KPADFR: 06EE A90C KPADF2: 06F0 8D1FD0 06F3 60 KPADF3: 06F4 A904 KPADF3: 06F6 8D1FD0 06F9 60				STA LDA STA	#\$C0	BRKPRS #\$C0 ;LOAD DUMMY KEYCODE KPADCOD					
				RTS LDA STA RTS LDA STA RTS	#OPTION CONSOL						
			#SELECT CONSOL								
06FA A	909 D1FD0	KPADF4:		LDA STA RTS	#STA						
		******	*****	••							
0700				END	COL	OST					
no ERROR	, 41 Labe	els, \$4732 fre	e.								
ALLPO ATTRA BPOT BREAI BRKPI CH	AC K RS	D208 004D 0008 0011 06BF 02FC	D 1#20 08 1#17 1 1#27 3F 3/18 FC 1#21	0 2/ 7 2/ 7 3/ 1 3/	40 61 41 8	2/41 3/58 3/26	3/62				
COLD	OL	0600 D01F	1#35 1#26	4/	10	4/13	4/16				
DOSIN EXIT EXIT1 KPAD KPAD		000C 06B9 069F 0651 0671	06B9 1/47 069F 3/11 0651 1/52	3/11 1/52	1/47 3/11 1/52	1/3 1/4 3# 1/3 2/3	49 15 53	1/37 2/52 3/29 2#28	1/41 3/20	1/43 3#33	3/57
KPADI KPADI KPADI KPADI KPADI KPADI	CO DM F1 F2 F3	06BD 069A 06DC 06EE 06F4 06FA	2/62 2/29 3/44 3/45 3/46 3/47	3/ 3# 3# 4# 4#		3/14	3#35	4/ 7			
KPAD KPAD	FR	06ED 06C0	3/59 3#44		8	3/53					
KPADFU KPADFV		06C8 06D6	D6 3/51	3/		3#56					
KPADI KPADI KPAD	RE	067F 06BE	2/48 3/10	3/2		3/28	3/30	3#36			
KPAD KPAD KPAD	TA VB	06A6 062E 0612 06B6	3/ 6 2# 9 1#46 3/25	2/4		2/51	2/59				
OPTIO PORTA SELEC SETVE START STRIG	N A CT BV	000C D300 000A E45C 0009 0285	1#16 1#23 1#15 1#24 1#14 1#19	4/ 2/3 4/ 1/3 4/ 2/3	9 33 12 55						
TEMP TIMER TIMER	1	06BC 0030 0006	2/38 1#12 1#13	2/4 3/ 3/3	43 9	3#34					
VVBL	(D	0224 0628	1#18 1/40	1/4	46	1/48 1#60					



A Warner Communications Company (W)



Every effort has been made to ensure the accuracy of the documentation in this manual. However, because ATARI, INC. is constantly improving and updating the computer software and hardware, we are unable to guarantee the accuracy of the printed material after the date of publication and disclaim liability for changes, errors or omissions.

No reproduction of this document or any portion of its contents is allowed without specific written permission of ATARI, INC., Sunnyvale, CA 94086.

PRINTED IN U.S.A. CO61037 REV. A