<table>
<thead>
<tr>
<th>R/W</th>
<th>ADR</th>
<th>HEXADR</th>
<th>NAME</th>
<th>DESCRIPTION</th>
<th>OS</th>
</tr>
</thead>
<tbody>
<tr>
<td>write only</td>
<td>54277</td>
<td>$D405</td>
<td>VSCROL</td>
<td>Vertical Fine Scrolling</td>
<td>both</td>
</tr>
</tbody>
</table>

If this location is set to a value other than zero, the ANTIC starts displaying every line with bit 5 set (enable vertical scrolling) that number of scan line "later", the picture is shifted "up".

This register should not be altered while ANTIC is drawing the screen. It has to be changed during vertical blank interrupt to get good results.

VSCROL=0  VSCROL=2

........  ..####..
....###  .####..
..####  .#####
.###.##  .####..
...##...  .##..##.
........  .####.
........  .##..##.
..####..  .######.
.##..##.  .##..##.
.######.  .##..##.
.##..##.  ........
.##..##.  ........
........  ........
........  ........
........  ........
........  ........
........  ........

If there are several consecutive lines set for vertical fine scrolling (display list instruction with bit 5 set to "1"), the last line to be scrolled has to have bit 5 set to 0, to inhibit lines "jumping" into the screen.

see also: Display List topics

previous: HSCROL

next: PMBASE